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**IN THE UNITED STATES
PATENT AND TRADEMARK OFFICE**

PATENT APPLICATION

Inventor(s): Kevin Loughran
Rui Adelino Silva
Joseph Veltri

Case: 9-3-2

Serial No.: 10/815129

Filing Date: March 31, 2004

Examiner: Group Art Unit: 2661

Title: Routing Architecture

COMMISSIONER FOR PATENTS

P.O. BOX 1450
ALEXANDRIA, VA 22313-1450

SIR:

INFORMATION DISCLOSURE STATEMENT
CERTIFICATION UNDER 37 CFR 1.97(c)(1)

In accordance with 37 CFR 1.97(c)(1), the enclosed Information Disclosure Statement, with attached reference(s), is submitted for consideration in the above-identified application.

I certify that each item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement. Copies of the listed documents are enclosed together with the search report that listed these documents.

NO FEE IS REQUIRED

In the event of any non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit **Lucent Technologies Deposit Account No. 12-2325** as required to correct the error.

Respectfully,

Ozer M.N. Teitelbaum, Attorney
Reg. No. 36698
973-386-8803

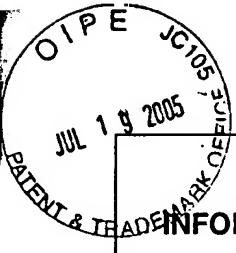
Date: 7/13/05

Docket Administrator (Room 3J-219)

Lucent Technologies Inc.
101 Crawfords Corner Road
Holmdel, NJ 07733-3030

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450, on July 15, 2005.

Rose E. Ewald



TRADEMARK OF INFORMATION DISCLOSURE STATEMENT	Case Name: K. Loughran 9-3-2 Serial No. 10/815129 Applicant: K. Loughran, et al. Filing Date: March 31, 2004 Group: 2661
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U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation
	AB	EP 1 187 497	3/13/2002	EPC	H04Q	3/00	Yes

OTHER (including Author, Title, Date, Pertinent Pages, etc.)

	AC	Intel Corporation, "Intel® IXP1200 Network Processor," <i>DATASHEET 'Online!'</i> , (12-01), URL: ftp://download.intel.com/design/network/datashts/27829810.pdf , pp. 1-25.
	AD	Intel Corporation, "IXB3208 IX Bus Scaling Fabric Device," <i>PRODUCT BRIEF, 'Online!'</i> , (11-7-00) URL: http://www.caps1.udel.edu/{fchen/projects/np/docs/manual/volumnl/IXB208%20Product%20Brief.pdf , pp 1-2.
	AE	European Search Report for Appl. No. 05251543.4-1416-, (6-27-05).

****References listed beyond AZ would list as AA-1, AB-2, AC-3 thru AZ-26*

*****Note First Page ONLY Header/Footer. Subsequent pages must ONLY have page # layout as header.**

EXAMINER	DATE CONSIDERED
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***Examiner:** Initial if reference considered, whether or not citation is in conformance with MPEP 609: Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant